## SL12BF~SL110BF 1.0Amp Schottky Barrier Rectifiers

## **Features**

- ◆ For surface mounted applications
- Low forward voltage drop
- Low power loss, high efficiency
- Construction utilizes void-free molded plastic technique
- ◆ High forward surge current capability
- ◆ High temperature soldering guaranteed: 260°C/10 seconds at terminals

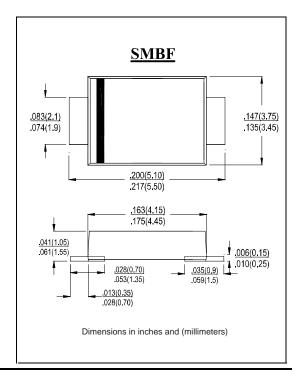
## **Mechanical Data**

Case: JEDEC SMBF molded plastic body

Terminals: Solder plated, solderable per MIL-STD-750, Method 2026

Polarity: Color band denotes cathode end

Mounting Position: Any



## **Maximum Ratings And Electrical Characteristics**

Ratings at  $25^{\circ}$ C ambient temperature unless otherwise specified. Single phase half-wave 60Hz, resistive or inductive load, for capacitive load current derate by 20%.

MDD Catalog Number	SYMBOLS	SL12BF	SL14BF	SL16BF	SL110BF	UNITS
Maximum repetitive peak reverse voltage	VRRM	20	40	60	100	VOLTS
Maximum RMS voltage	V <sub>RMS</sub>	14	28	42	70	VOLTS
Maximum DC blocking voltage	VDC	20	40	60	100	VOLTS
Maximum average forward rectified current 0.375" (9.5mm) lead length (see fig.1)	I(AV)	1.0				Amps
Peak forward surge current 8.3ms single half sine-wave superimposed on rated load (JEDEC Method)	Ігѕм	30.0				Amps
Maximum instantaneous T <sub>A</sub> =25℃	V <sub>F</sub>	0.40	0.45	0.55	0.70	Volts
forward voltage at 1.0A T <sub>A</sub> =125℃		0.35	0.40	0.50	0.62	
Maximum DC reverse current TA=25℃	I <sub>R</sub>		1.0		0.5	mA
at rated DC blocking voltage TA=125℃		50.0 20.0				
Typical junction capacitance (NOTE 1)	CJ	100				pF
Typical thermal resistance (NOTE 2)	RθJA	75				°C/W
Operating junction temperature range	TJ,	-50 to +125				°C

Note: 1. Measured at 1MHz and applied reverse voltage of 4.0 VD.C.

2.P.C.B. mounted with 0.2x0.2 (5.0x5.0mm) copper pad areas