

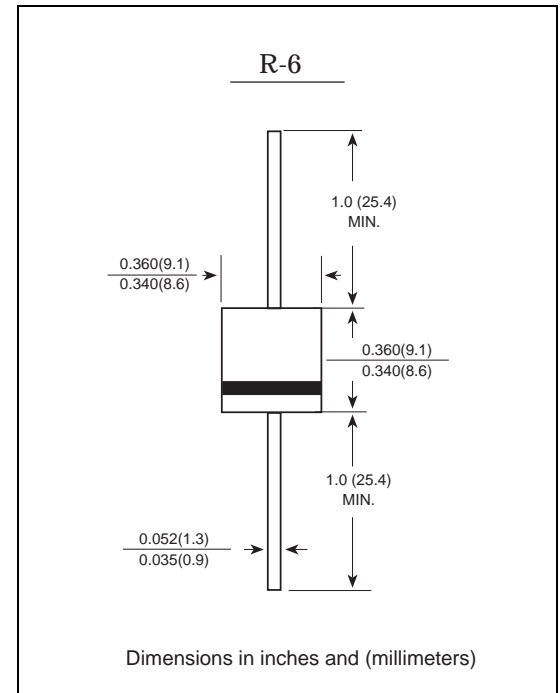
8A05 ~8A10 8.0Amp Silicon Rectifiers

Features

- ◆ The plastic package carries Underwriters Laboratory Flammability Classification 94V-0
- ◆ Open-Junction chip ,silastic passivated
- ◆ Low reverse leakage
- ◆ High forward surge current capability
- ◆ High temperature soldering guaranteed
250°C/10 seconds at terminals

Mechanical Data

Case: JEDEC R-6 molded plastic body
 Terminals: Solder plated, solderable per MIL-STD-750, Method 2026
 Polarity: Color band denotes cathode end
 Mounting Position: Any
 Weight : 0.072 ounce, 2.05 grams



Maximum Ratings And Electrical Characteristics

Ratings at 25°C ambient temperature unless otherwise specified. Single phase half-wave 60Hz, resistive or inductive load, for capacitive load current derate by 20%.

	SYMBOLS	8A05	8A1	8A2	8A3	8A4	8A6	8A8	8A10	UNITS
Maximum repetitive peak reverse voltage	V_{RRM}	50	100	200	300	400	600	800	1000	VOLTS
Maximum RMS voltage	V_{RMS}	35	70	140	210	280	420	560	700	VOLTS
Maximum DC blocking voltage	V_{DC}	50	100	200	300	400	600	800	1000	VOLTS
Maximum average forward rectified current at $T_L=60^\circ\text{C}$	$I_{(AV)}$	8.0								Amp
Peak forward surge current 8.3ms single half sine-wave superimposed on rated load (JEDEC Method)	I_{FSM}	200								Amps
Maximum instantaneous forward voltage at 8.0A	V_F	1.0								Volts
Maximum DC reverse current $T_A=25^\circ\text{C}$ at rated DC blocking voltage $T_A=100^\circ\text{C}$	I_R	10.0 100.0								μA
Typical junction capacitance (Note 2)	C_J	120								pF
Typical thermal resistance (Note 3)	R_{qJA}	20								$^\circ\text{C}/\text{W}$
Operating junction	T_J, T_{STG}	-55 to +125								$^\circ\text{C}$
Storage temperature range	T_J, T_{STG}	-55 to +150								$^\circ\text{C}$

Note: 1.Reverse recovery time test condition: $I_F=0.5\text{A}$ $I_R=1.0\text{A}$ $I_{rr}=0.25\text{A}$
 2.Measured at 1MHz and applied reverse voltage of 4.0V D.C.
 3.Thermal resistance from junction to ambient at 0.375 "(9.5mm)lead length,P.C.B. mounted