

ES1AFL~ES1JFL

1.0Amp Super Fast Recovery Surface Mounted Rectifiers

Features

- The plastic package carries Underwriters Laboratory Flammability Classification 94V-0
- Idea for printed circuit board
- Glass passivated Junction chip
- Low reverse leakage
- High forward surge current capability
- High temperature soldering guaranteed
250°C/10 seconds at terminals

Mechanical Data

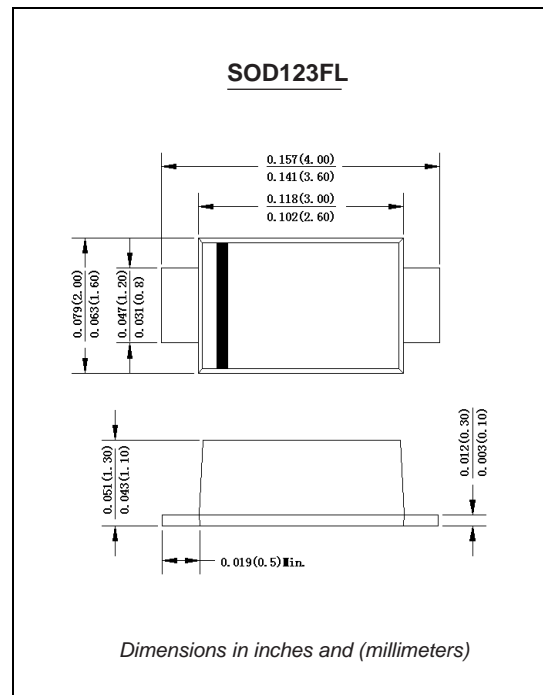
Case: JEDEC SOD-123FL molded plastic body

Terminals: Solder plated, solderable per MIL-STD-750, Method 2026

Polarity: Polarity symbol marking on body

Mounting Position: Any

Weight : 0.0007 ounce, 0.02grams



Maximum Ratings And Electrical Characteristics

Ratings at 25°C ambient temperature unless otherwise specified. Single phase half-wave 60Hz, resistive or inductive load, for capacitive load current derate by 20%.

	SYMBOLS	ES1AFL	ES1BFL	ES1CFL	ES1DFL	ES1JEL	ES1GFL	ES1JFL	UNITS
	Marking Code	E1	E2	E3	E4	E5	E6	E7	
Maximum repetitive peak reverse voltage	V_{RRM}	50	100	150	200	300	400	600	VOLTS
Maximum RMS voltage	V_{RMS}	35	70	105	140	210	280	420	VOLTS
Maximum DC blocking voltage	V_{DC}	50	100	150	200	300	400	600	VOLTS
Maximum average forward rectified current at $T_L=60^\circ\text{C}$	$I_{(AV)}$	1.0							Amp
Peak forward surge current 8.3ms single half sine-wave superimposed on rated load (JEDEC Method)	I_{FSM}	30.0							Amps
Maximum instantaneous forward voltage at 1A	V_F	0.95			1.25		1.70		Volts
Maximum DC reverse current $T_A=25^\circ\text{C}$ at rated DC blocking voltage $T_A=125^\circ\text{C}$	I_R	5.0 500							μA
Maximum reverse recovery time(Note 1)	T_{rr}	35							ns
Typical junction capacitance (Note 2)	C_J	15.0							pF
Typical thermal resistance (Note 3)	R_{qJA}	60.0							$^\circ\text{C/W}$
Operating junction and storage temperature range	T_J, T_{STG}	-50 to +155							$^\circ\text{C}$

Note: 1.Reverse recovery time test condition: $I_F=0.5\text{A}$ $I_R=1.0\text{A}$ $I_{rr}=0.25\text{A}$

2.Measured at 1MHz and applied reverse voltage of 4.0V D.C.

3.Mounted on copper PC board with 5.0*5.0mm solder pad

Ratings And Characteristic Curves

ES1AFL THRU ES1JFL

FIG. 1- DERATING CURVE OUTPUT RECTIFIED CURRENT

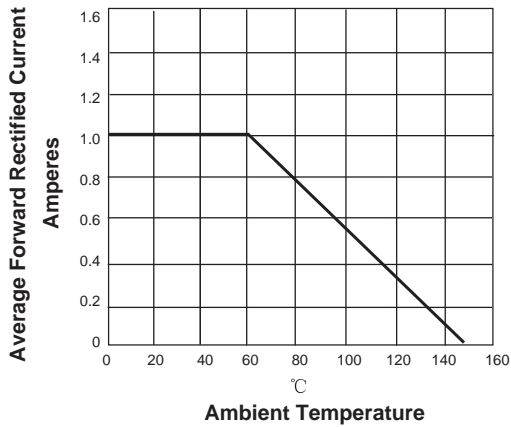


FIG. 2-MAXIMUM NON-REPETITIVE PEAK FORWARD SURGE CURRENT PER LEG

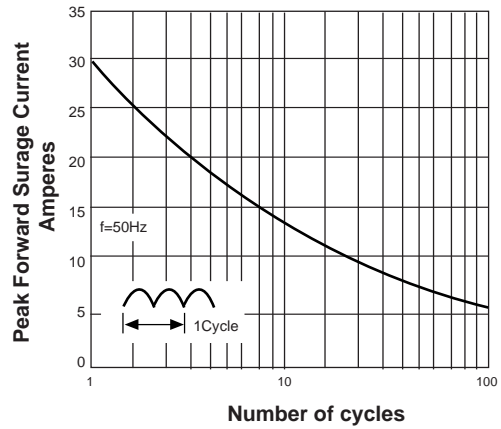


FIG. 3-TYPICAL FORWARD VOLTAGE CHARACTERISTICS

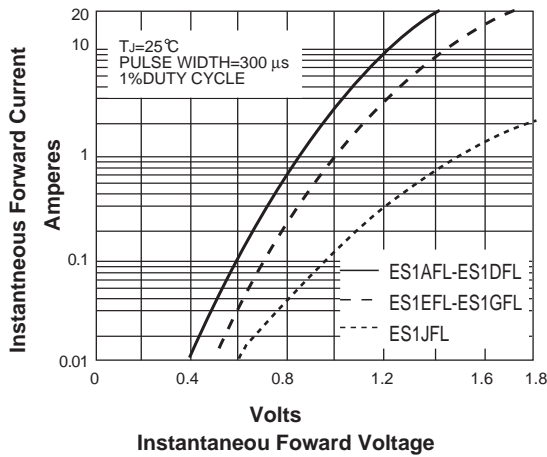


FIG. 4-TYPICAL REVERSE LEAKAGE CHARACTERISTICS

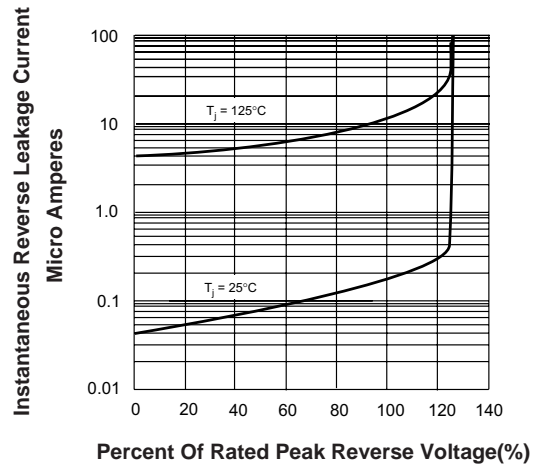


FIG. 5-TYPICAL JUNCTION CAPACITANCE

